

Figures

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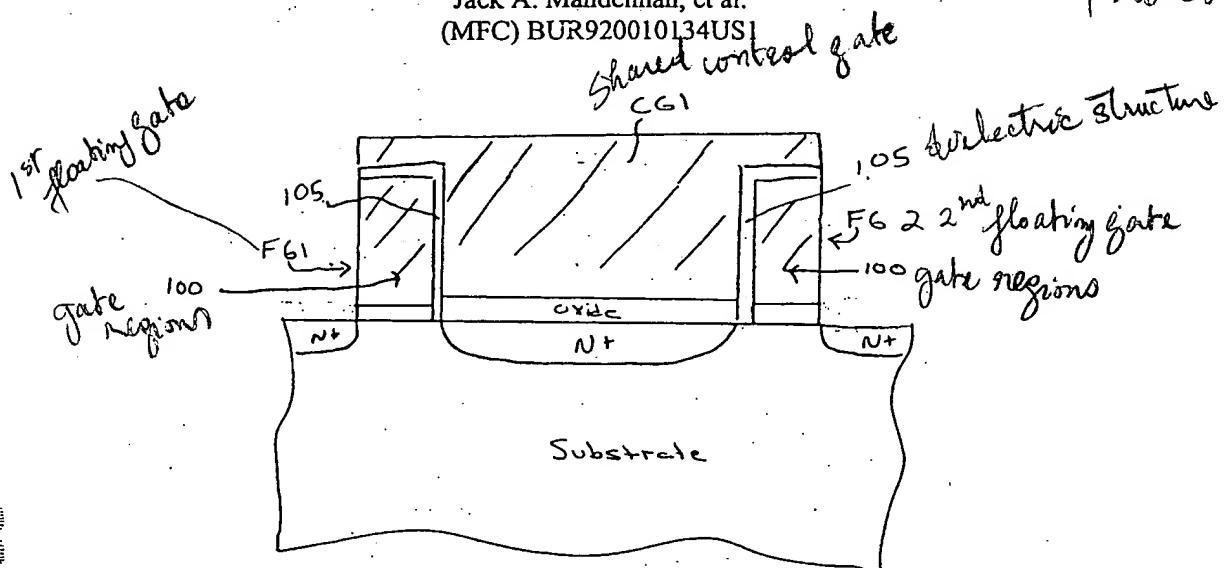


FIG 1 programmable memory cell structure

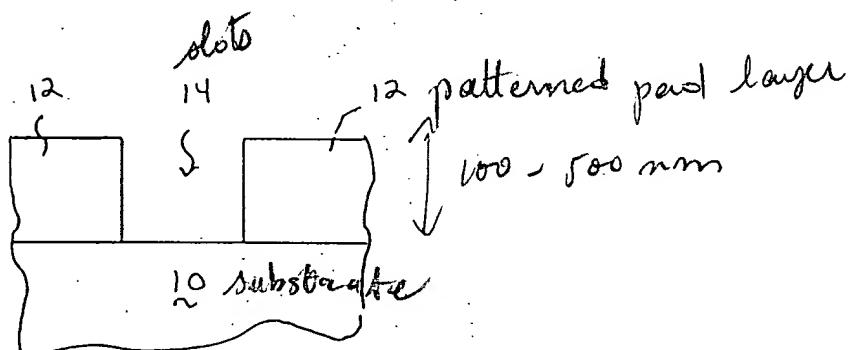


FIG 2

12 dots
 14
 10 substrate
 12 patterned pad layer
 100 - 500 nm

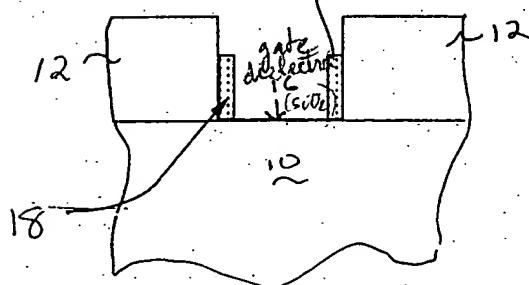


FIG 3

High-density plasma (HDP) oxide

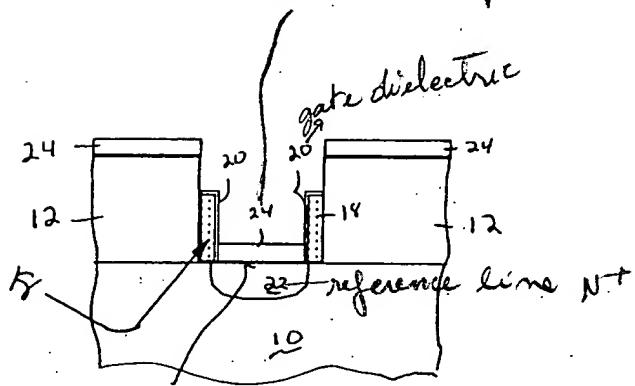


FIG 4

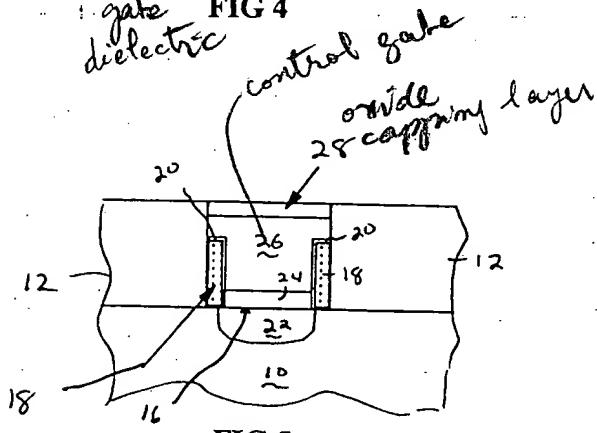


FIG 5

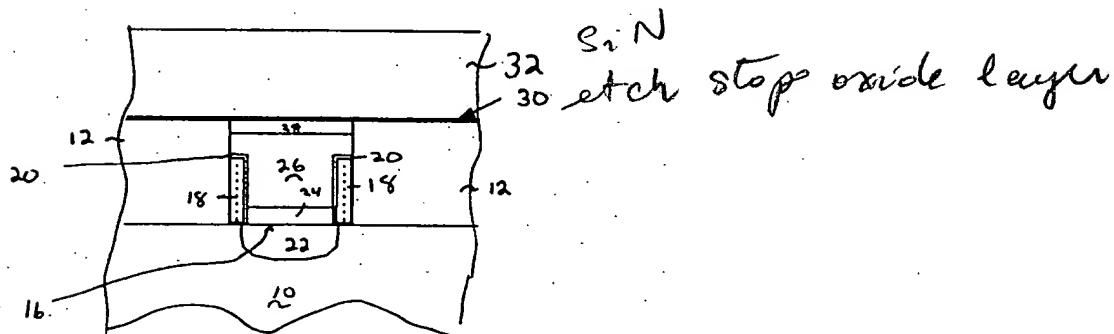


FIG 6

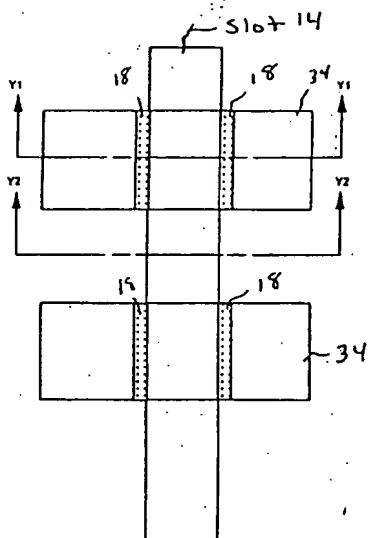


FIG 7A

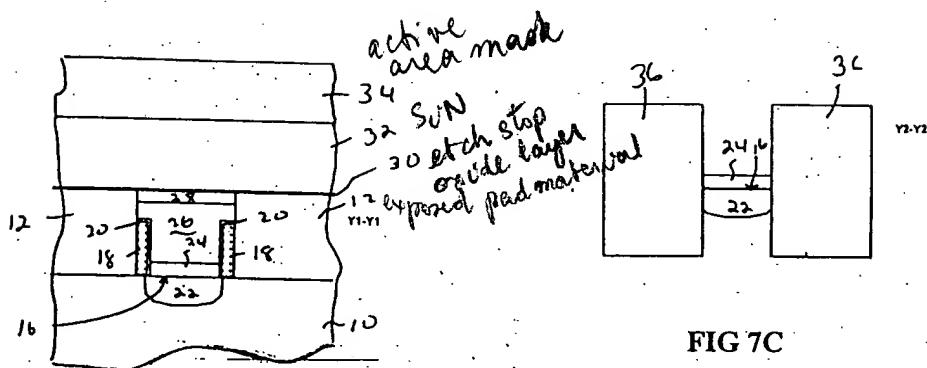


FIG 7C

FIG 7B

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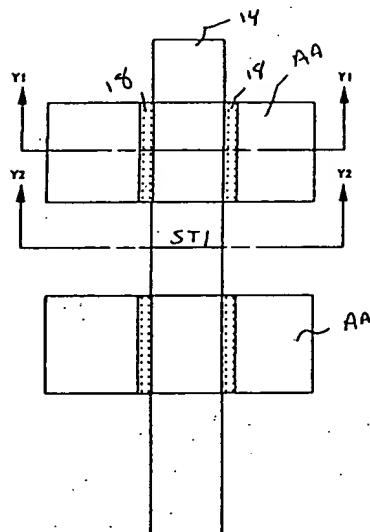


FIG 8A

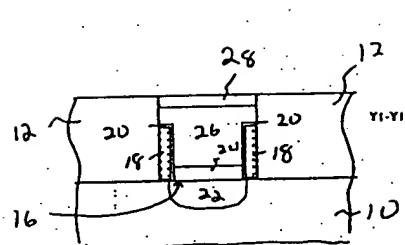


FIG 8B

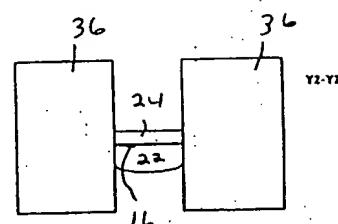


FIG 8C

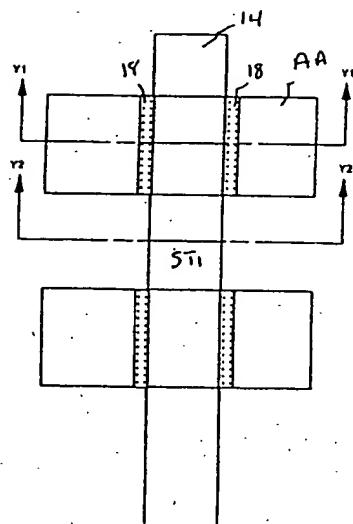


FIG 9A

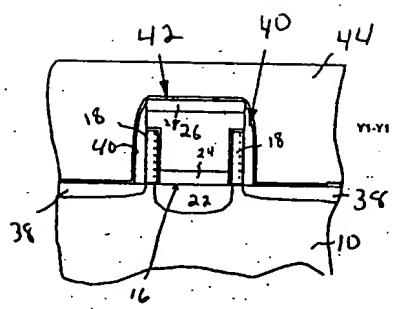


FIG 9B

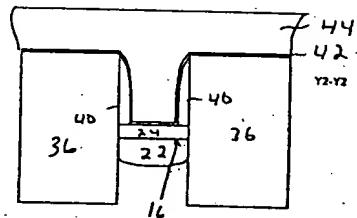


FIG 9C

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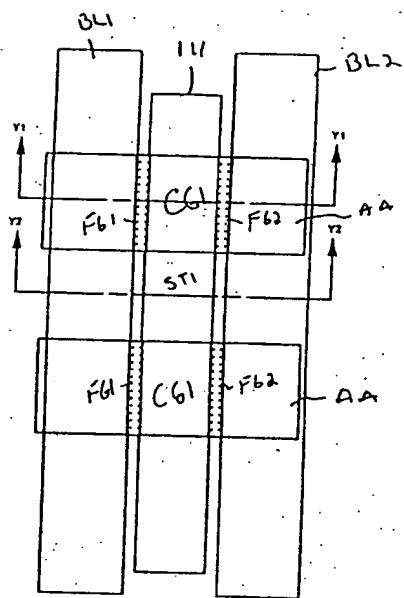


FIG 10A

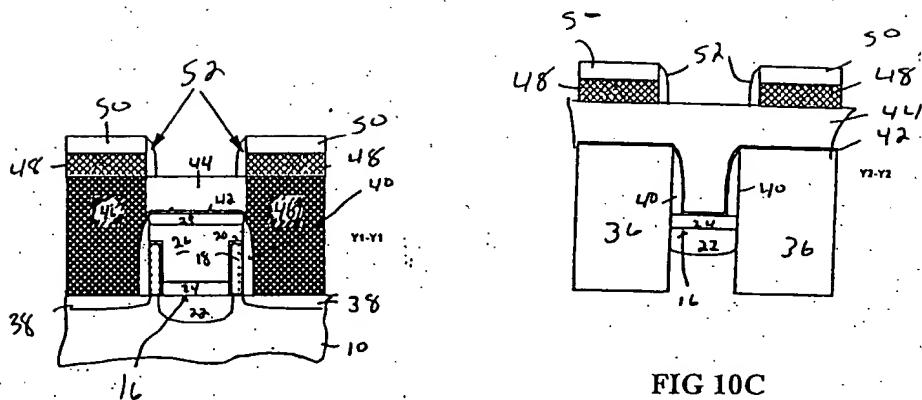


FIG 10C

FIG 10B

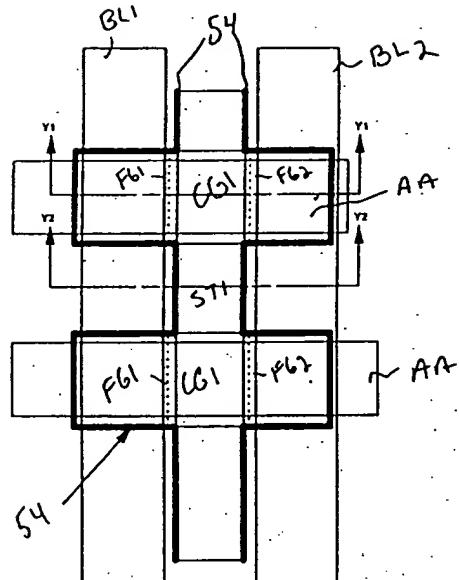


FIG 11A

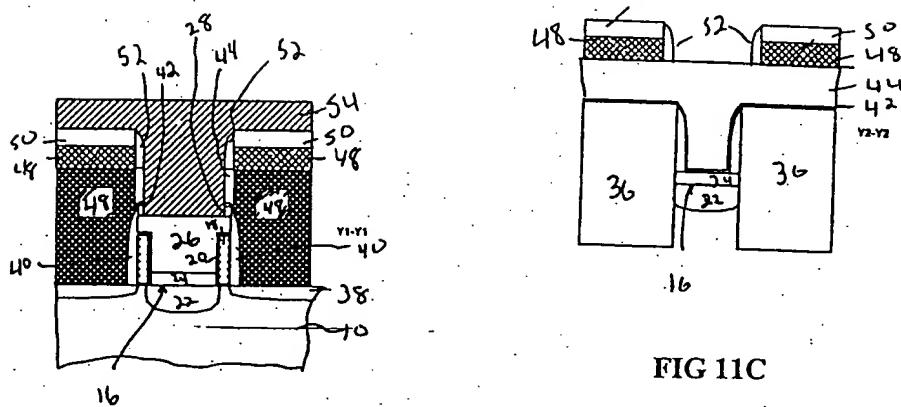


FIG 11B

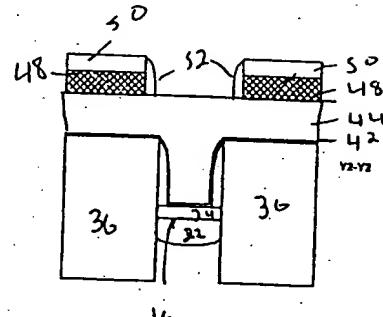


FIG 11C

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BUR920010134US1

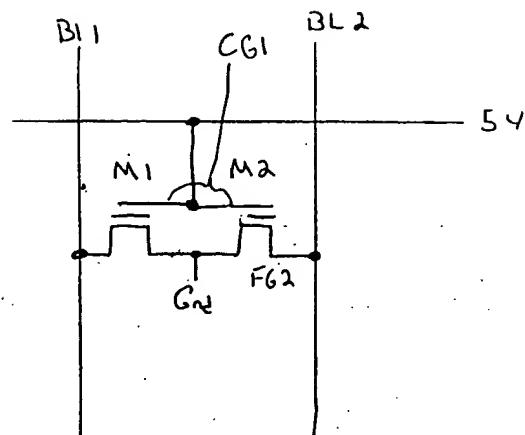


FIG 12